

# Deciding $k$ CFA is complete for EXPTIME\*

David Van Horn    Harry G. Mairson

Brandeis University

{dvanhorn,mairson}@cs.brandeis.edu

## Abstract

We give an exact characterization of the computational complexity of the  $k$ CFA hierarchy. For any  $k > 0$ , we prove that the control flow decision problem is complete for deterministic exponential time. This theorem validates empirical observations that such control flow analysis is intractable. It also provides more general insight into the complexity of abstract interpretation.

**Categories and Subject Descriptors** F.3.2 [Logics and Meanings of Programs]: Semantics of Programming Languages—Program analysis; F.4.1 [Mathematical Logic and Formal Languages]: Mathematical Logic—Computability theory, Computational logic, Lambda calculus and related systems

**General Terms** Languages, Theory

**Keywords** flow analysis, complexity

## 1. Introduction

Flow analysis (Jones 1981; Sestoft 1988; Shivers 1988; Midtgaard 2007) is concerned with the sound approximation of run-time values at compile time. This analysis gives rise to natural decision problems such as: *does expression  $e$  possibly evaluate to value  $v$  at run-time?* or *does function  $f$  possibly get applied at call site  $\ell$ ?*<sup>1</sup> The most approximate analysis always answers *yes*. This crude “analysis” takes no resources to compute, and is useless. In complete contrast, the most precise analysis only answers *yes* by running the program to find that out. While such information is surely useful, the cost of this analysis is likely prohibitive, requiring intractable or unbounded resources. Practical flow analyses occupy a niche between these extremes, and their *expressiveness* can be characterized by the computational resources required to compute their results.

Examples of simple yet useful flow analyses include Shivers’ OCFA (1988) and Henglein’s simple closure analysis (1992), which are *monovariant*—functions that are closed over the same  $\lambda$ -expression are identified. Their expressiveness is characterized by the class PTIME (Van Horn and Mairson 2007, 2008). More precise analyses can be obtained by incorporating context-sensitivity to distinguish multiple closures over the same  $\lambda$ -term. The  $k$ CFA

hierarchy uses the last  $k$  calling contexts to distinguish closures, resulting in “finer grained approximations, expending more work to gain more information” (Shivers 1988, 1991).

The increased precision comes with an empirically observed increase in cost. As Shivers noted in his retrospective on the  $k$ CFA work (2004):

*It did not take long to discover that the basic analysis, for any  $k > 0$ , was intractably slow for large programs. In the ensuing years, researchers have expended a great deal of effort deriving clever ways to tame the cost of the analysis.*

A fairly straightforward calculation—see, for example, Nielson et al. (1999)—shows that OCFA can be computed in polynomial time, and for any  $k > 0$ ,  $k$ CFA can be computed in exponential time. These naive upper bounds suggest that the  $k$ CFA hierarchy is essentially *flat*; researchers subsequently “expended a great deal of effort” trying to improve them.<sup>2</sup> For example, it seemed plausible (at least, to us) that the  $k$ CFA problem could be in NP by *guessing* flows appropriately during analysis.

In this paper, we show that the naive algorithm is essentially the best one, and that the *lower* bounds are what needed improving. We prove that for all  $k > 0$ , computing the  $k$ CFA analysis requires (and is thus complete for) deterministic exponential time. There is, in the worst case—and plausibly, in practice—no way to tame the cost of the analysis. Exponential time is required.

Who cares, and why should this result matter to functional programmers?

This result concerns a fundamental and ubiquitous static analysis of functional programs. The theorem gives an analytic, scientific characterization of the expressive power of  $k$ CFA. As a consequence, the *empirically observed* intractability of the cost of this analysis can be understood as being *inherent in the approximation problem being solved*, rather than reflecting unfortunate gaps in our programming abilities.

Good science depends on having relevant theoretical understandings of what we observe empirically in practice—otherwise, we devolve to an unfortunate situation resembling an old joke about the difference between geology (the observation of physical phenomena that cannot be explained) and geophysics (the explanation of physical phenomena that cannot be observed). Or worse—scientific aberrations such as alchemy, or Ptolemaic epicycles in astronomy.

This connection between theory and experience contrasts with the similar result for ML type inference (Mairson 1990): we confess that while the problem of recognizing ML-typable terms is complete for exponential time, programmers have happily gone on programming. It is likely that their need of higher-order procedures, essential for the lower bound, is not considerable. But static flow analysis really has been costly, and our theorem explains why.

\* Work supported under NSF Grant CCF-0811297.

<sup>1</sup> There is a bit more nuance to the question, which will be developed later.

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. To copy otherwise, to republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee.

ICFP’08, September 22–24, 2008, Victoria, BC, Canada.  
Copyright © 2008 ACM 978-1-59593-919-7/08/09...\$5.00

<sup>2</sup> Even so, there is a big difference between algorithms that run in  $2^n$  and  $2^{n^2}$  steps, though both are nominally in EXPTIME.

The theorem is proved *by* functional programming. We take the view that the analysis itself is a functional programming language, albeit with implicit bounds on the available computational resources. Our result harnesses the approximation inherent in *k*CFA as a computational tool to hack exponential time Turing machines within this unconventional language. The hack used here is completely unlike the one used for the ML analysis, which depended on complete developments of `let`-redexes. The theorem we prove in this paper uses approximation in a way that has little to do with normalization.

## 2. Preliminaries

### 2.1 Instrumented interpretation

*k*CFA can be thought of as an abstraction (in the sense of a computable approximation) to an instrumented interpreter, which not only evaluates a program, but records a history of *flows*. Every time a subterm evaluates to a value, every time a variable is bound to a value, the *flow* is recorded. Consider a simple example, where  $e$  is closed and in normal form:

$$(\lambda x.x)e$$

We label the term to index its constituents:

$$((\lambda x.x^0)^1 e^2)^3$$

The interpreter will first record all the flows for evaluating  $e$  (there are none, since it is in normal form), then the flow of  $e$ 's value (which is  $e$ , closed over the empty environment) into label 2 ( $e$ 's label) is recorded.

This value is then recorded as flowing into the binding of  $x$ . The body of the  $\lambda x$  expression is evaluated under an extended environment with  $x$  bound to the result of evaluating  $e$ . Since it is a variable occurrence, the value bound to  $x$  is recorded as flowing into this variable occurrence, labeled 0. Since this is the result of evaluating the body of the function, it is recorded as flowing out of the  $\lambda$ -term labeled 1. And again, since this is the result of the application, the result is recorded for label 3.

The flow history is recorded in a *cache*,  $C$ , which maps labels and variables to values. If the cache maps a variable to a value,  $C(x) = v$ , it means that during evaluation of the program, the variable  $x$  was bound to the value  $v$  at some point. If the cache maps a label to a value,  $C(\ell) = v$ , it means that the subexpression with that label evaluated to that value.

Of course, a variable may be bound to any number of values during the course of evaluation. Likewise, a subexpression that occurs once syntactically may evaluate to any number of values during evaluation. So asking about the flows of a subexpression is ambiguous without further information. Our simple example does not reflect this possible ambiguity, but consider the following example, where `True` and `False` are closed and in normal form:

$$(\lambda f.f(f \text{ True}))(\lambda y.\text{False})$$

During evaluation,  $y$  gets bound to both `True` and `False`—asking “what was  $y$  bound to?” is ambiguous. But let us label the applications in our term:

$$((\lambda f.(f(f \text{ True})^1)^2)(\lambda y.\text{False})^3)^3$$

Notice that  $y$  is bound to different values within different contexts. That is,  $y$  is bound `True` when evaluating the application labeled 1, and to `False` when evaluating the application labeled 2. Both of these occur while evaluating the outermost application, labeled 3. A string of these application labels, called a *contour*, uniquely describes the *context* under which a subexpression evaluates.

$$\begin{aligned} 3 \cdot 2 \cdot 1 & \text{ describes } ((\lambda f.(f[ ]^1)^2)(\lambda y.\text{False}))^3 \\ 3 \cdot 2 & \text{ describes } ((\lambda f.[ ]^2)(\lambda y.\text{False}))^3 \end{aligned}$$

So a question about what a subexpression evaluates to *within a given context* has an unambiguous answer. The interpreter, therefore, maintains an environment that maps each variable to a description of the context in which it was bound. Similarly, flow questions about a particular subexpression or variable binding must be accompanied by a description of a context. Returning to our example, we would have  $C(y, 3 \cdot 2 \cdot 1) = \text{True}$  and  $C(y, 3 \cdot 2) = \text{False}$ .

Typically, values are denoted by *closures*—a  $\lambda$ -term together with an environment mapping all free variables in the term to values.<sup>3</sup> But in the instrumented interpreter, rather than mapping variables to values, environments map a variable to a *contour*—the sequence of labels which describes the context of successive function applications in which this variable was bound:<sup>4</sup>

$$\begin{aligned} \delta & \in \Delta & = & \mathbf{Lab}^n & \text{ contour} \\ ce & \in \mathbf{CEnv} & = & \mathbf{Var} \rightarrow \Delta & \text{ contour environment} \end{aligned}$$

By consulting the cache, we can then retrieve the value. So if under typical evaluation, a term labeled  $\ell$  evaluates to  $\langle \lambda x.e, \rho \rangle$  within a context described by a string of application labels,  $\delta$ , then we will have  $C(\ell, \delta) = \langle \lambda x.e, ce \rangle$ , where the contour environment  $ce$ , like  $\rho$ , closes  $\lambda x.e$ . But unlike  $\rho$ , it maps each variable to a contour describing the context in which the variable was bound. So if  $\rho(y) = \langle \lambda z.e', \rho' \rangle$ , then  $C(y, ce(y)) = \langle \lambda z.e', ce' \rangle$ , where  $\rho'$  is similarly related to  $ce'$ .

We can now write the instrumented evaluator. The syntax of the language is given by the following grammar:

$$\begin{aligned} \mathbf{Exp} \quad e & ::= t^\ell & \text{ expressions (or labeled terms)} \\ \mathbf{Term} \quad t & ::= x \mid e \mid \lambda x.e & \text{ terms (or unlabeled expressions)} \end{aligned}$$

$\mathcal{E}[[t^\ell]_\delta^{ce}]$  evaluates  $t$  and writes the result into the table  $C$  at location  $(\ell, \delta)$ . The notation  $C(\ell, \delta) \leftarrow v$  means that the cache is updated so that  $C(\ell, \delta) = v$ . The notation  $\delta\ell$  denotes the concatenation of contour  $\delta$  and label  $\ell$ .

$$\begin{aligned} \mathcal{E}[[x^\ell]_\delta^{ce}] & = C(\ell, \delta) \leftarrow C(x, ce(x)) \\ \mathcal{E}[[\lambda x.e]^\ell]_\delta^{ce} & = C(\ell, \delta) \leftarrow \langle \lambda x.e, ce' \rangle \\ & \quad \text{where } ce' = ce \upharpoonright \mathbf{fv}(\lambda x.e) \\ \mathcal{E}[[t^{\ell_1} t^{\ell_2}]^\ell]_\delta^{ce} & = \mathcal{E}[[t^{\ell_1}]_\delta^{ce}]; \mathcal{E}[[t^{\ell_2}]_\delta^{ce}]; \\ & \quad \text{let } \langle \lambda x.t^{\ell_0}, ce' \rangle = C(\ell_1, \delta) \text{ in} \\ & \quad C(x, \delta\ell) \leftarrow C(\ell_2, \delta); \\ & \quad \mathcal{E}[[t^{\ell_0}]_\delta^{ce'}]_{\delta\ell} \\ & \quad C(\ell, \delta) \leftarrow C(\ell_0, \delta\ell) \end{aligned}$$

The cache constructed by

$$\mathcal{E}[[((\lambda f.(f(f \text{ True})^1)^2)(\lambda y.\text{False})^3)]_\emptyset^0]$$

includes the following entries:

$$\begin{aligned} C(f, 3) & = \lambda y.\text{False} \\ C(y, 3 \cdot 2 \cdot 1) & = \text{True} \\ C(1, 3 \cdot 2) & = \lambda y.\text{False} \\ C(y, 3 \cdot 2) & = \text{False} \end{aligned}$$

In a more declarative style, we can write a specification of *acceptable caches*—a cache is acceptable iff it records all of the flows which occur during evaluation. The smallest cache satisfying this acceptability relation is the one that is computed by the above

<sup>3</sup> We abused syntax above by writing the closure of a closed term as the term itself, rather than  $\langle \text{True}, \emptyset \rangle$ , for example. We continue with the abuse.

<sup>4</sup> All of the syntactic categories are implicitly understood to be restricted to the finite set of terms, labels, variables, etc. that occur in the *program of interest*—the program being analyzed. As a convention, programs are assumed to have distinct bound variable names and labels.

interpreter.

$$\begin{aligned}
C \models_{\delta}^{ce} x^{\ell} & \text{ iff } C(x, ce(x)) = C(\ell, \delta) \\
C \models_{\delta}^{ce} (\lambda x.e)^{\ell} & \text{ iff } \langle \lambda x.e, ce' \rangle = C(\ell, \delta) \\
& \text{ where } ce' = ce \upharpoonright \mathbf{fv}(\lambda x.e) \\
C \models_{\delta}^{ce} (t^{\ell_1} t^{\ell_2})^{\ell} & \text{ iff } C \models_{\delta}^{ce} t^{\ell_1} \wedge C \models_{\delta}^{ce} t^{\ell_2} \wedge \\
& \text{ let } \langle \lambda x.t^{\ell_0}, ce' \rangle = C(\ell_1, \delta) \text{ in} \\
& C(\ell_2, \delta) = C(x, \delta\ell) \wedge \\
& C \models_{\delta\ell}^{ce' [x \mapsto \delta\ell]} t^{\ell_0} \wedge \\
& C(\ell_0, \delta\ell) = C(\ell, \delta)
\end{aligned}$$

Clearly, because constructing a cache  $C$  is equivalent to evaluating a program, such a cache is not effectively computable. The next section describes  $k$ CFA as a computable *approximation*.

## 2.2 An abstract interpreter

$k$ CFA is a computable approximation to this instrumented interpreter. Rather than constructing an *exact* cache  $C$ , it constructs an *abstract* cache  $\widehat{C}$ , which maps labels and variables, not to values, but to *sets of abstract values*.

$$\begin{aligned}
\hat{v} & \in \widehat{\mathbf{Val}} = \mathcal{P}(\mathbf{Term} \times \mathbf{CEnv}) \\
\widehat{C} & \in \widehat{\mathbf{Cache}} = (\mathbf{Lab} + \mathbf{Var}) \times \Delta \rightarrow \widehat{\mathbf{Val}}
\end{aligned}$$

Approximation arises from contours being bounded at length  $k$ . If during the course of instrumented evaluation, the length of the contour would exceed length  $k$ , then the  $k$ CFA abstract interpreter will truncate it to length  $k$ . In other words, only a partial description of the context can be given, which results in ambiguity. A subexpression may evaluate to two distinct values, but within contexts which are only distinguished by  $k + 1$  labels. Questions about which value the subexpression evaluates to can only supply  $k$  labels, so the answer must be *both*, according to a sound approximation.

When applying a function, there is now a set of possible closures that flow into the operator position. Likewise, there can be a multiplicity of arguments. What is the interpreter to do? The abstract interpreter applies all possible closures to all possible arguments.

The abstract interpreter, the imprecise analog of  $\mathcal{E}$ , is then:

$$\begin{aligned}
\mathcal{A}[[x^{\ell}]_{\delta}^{ce}] & = \widehat{C}(\ell, \delta) \leftarrow \widehat{C}(x, ce(x)) \\
\mathcal{A}[(\lambda x.e)^{\ell}]_{\delta}^{ce} & = \widehat{C}(\ell, \delta) \leftarrow \{ \langle \lambda x.e, ce' \rangle \} \\
& \text{ where } ce' = ce \upharpoonright \mathbf{fv}(\lambda x.e) \\
\mathcal{A}[(t^{\ell_1} t^{\ell_2})^{\ell}]_{\delta}^{ce} & = \mathcal{A}[[t^{\ell_1}]_{\delta}^{ce}]; \mathcal{A}[[t^{\ell_2}]_{\delta}^{ce}]; \\
& \text{ foreach } \langle \lambda x.t^{\ell_0}, ce' \rangle \in \widehat{C}(\ell_1, \delta) : \\
& \quad \widehat{C}(x, [\delta\ell]_k) \leftarrow \widehat{C}(\ell_2, \delta); \\
& \quad \mathcal{A}[[t^{\ell_0}]_{[\delta\ell]_k}^{ce' [x \mapsto [\delta\ell]_k]}]; \\
& \quad \widehat{C}(\ell_0, \delta) \leftarrow \widehat{C}(\ell_0, [\delta\ell]_k)
\end{aligned}$$

We write  $\widehat{C}(\ell, \delta) \leftarrow \hat{v}$  to indicate an updated cache where  $(\ell, \delta)$  maps to  $\widehat{C}(\ell, \delta) \cup \hat{v}$ . The notation  $[\delta]_k$  denotes  $\delta$  truncated to the rightmost (i.e., most recent)  $k$  labels.

Compared to the exact evaluator, contours similarly distinguish evaluation within contexts described by as many as  $k$  application sites: beyond this, the distinction is blurred. The imprecision of the analysis requires that  $\mathcal{A}$  be iterated until the cache reaches a fixed point, but care must taken to avoid looping in an iteration since a single iteration of  $\mathcal{A}[[e]_{\delta}^{ce}]$  may in turn make a recursive call to  $\mathcal{A}[[e]_{\delta}^{ce}]$  under the same contour and environment. This care is the algorithmic analog of appealing to the coinductive hypothesis in judging an analysis acceptable. These judgment rules are given below.

An acceptable  $k$ -level control flow analysis for an expression  $e$  is written  $\widehat{C} \models_{\delta}^{ce} e$ , which states that  $\widehat{C}$  is an acceptable analysis of

$e$  in the context of the current environment  $ce$  and current contour  $\delta$  (for the top level analysis of a program, these will both be empty).

Just as we did in the previous section, we can write a specification of acceptable caches rather than an algorithm that computes. The resulting specification is what is found, for example, in Nielson et al. (1999):

$$\begin{aligned}
\widehat{C} \models_{\delta}^{ce} x^{\ell} & \text{ iff } \widehat{C}(x, ce(x)) \subseteq \widehat{C}(\ell, \delta) \\
\widehat{C} \models_{\delta}^{ce} (\lambda x.e)^{\ell} & \text{ iff } \langle \lambda x.e, ce' \rangle \in \widehat{C}(\ell, \delta) \\
& \text{ where } ce' = ce \upharpoonright \mathbf{fv}(\lambda x.e) \\
\widehat{C} \models_{\delta}^{ce} (t^{\ell_1} t^{\ell_2})^{\ell} & \text{ iff } \widehat{C} \models_{\delta}^{ce} t^{\ell_1} \wedge \widehat{C} \models_{\delta}^{ce} t^{\ell_2} \wedge \\
& \forall \langle \lambda x.t^{\ell_0}, ce' \rangle \in \widehat{C}(\ell_1, \delta) : \\
& \quad \widehat{C}(\ell_2, \delta) \subseteq \widehat{C}(x, [\delta\ell]_k) \wedge \\
& \quad \widehat{C} \models_{[\delta\ell]_k}^{ce' [x \mapsto [\delta\ell]_k]} t^{\ell_0} \wedge \\
& \quad \widehat{C}(\ell_0, [\delta\ell]_k) \subseteq \widehat{C}(\ell, \delta)
\end{aligned}$$

The acceptability relation is given by the greatest fixed point of the functional defined according to the above clauses—and we are concerned only with least solutions.<sup>5</sup>

## 2.3 Complexity of abstract interpretation

What is the difficulty of computing within this hierarchy? What are the sources of approximation that render such analysis (in)tractable? We consider these questions by analyzing the complexity of the following decision problem:

**Control Flow Problem:** Given an expression  $e$ , an abstract value  $\{v\}$ , and a pair  $(\ell, \delta)$ , is  $v \in \widehat{C}(\ell, \delta)$  in the flow analysis of  $e$ ?

Obviously, we are interested in the complexity of control flow analysis, but our investigation also provides insight into a more general subject: the complexity of computing via abstract interpretation. It stands to reason that as the computational domain becomes more refined, so too should computational complexity. In this instance, the domain is the size of the abstract cache  $\widehat{C}$  and the values (namely, *closures*) that can be stored in the cache. As the table size and number of closures increase<sup>6</sup>, so too should the complexity of computation. From a theoretical perspective, we would like to understand better the tradeoffs between these various parameters.

## 3. Linearity and Boolean logic

It is straightforward to observe that in a *linear*  $\lambda$ -term, where each variable occurs at most once, each abstraction  $\lambda x.e$  can be applied to at most one argument, and hence the abstracted value can be bound to at most one argument. (Note that this observation is clearly untrue for the *nonlinear*  $\lambda$ -term  $(\lambda f.f(a(fb)))(\lambda x.x)$ , as  $x$  is bound to  $b$ , and also to  $ab$ .) Generalizing this observation, analysis of a linear  $\lambda$ -term coincides exactly with its evaluation:

LEMMA 1. *For any closed, linear expression  $e$ ,  $\mathcal{E}[[e]_{\delta}^0] = \mathcal{A}[[e]_{\delta}^0]$ , and thus  $C = \widehat{C}$ .*

A detailed proof of this lemma appears in Van Horn and Mairson (2008).

A natural and expressive class of such linear terms are the ones which implement Boolean logic. When we analyze the coding of a Boolean circuit and inputs to it, the Boolean output will flow to a

<sup>5</sup>To be precise, we take as our starting point *uniform*  $k$ CFA (Nielson and Nielson 1997) rather than a  $k$ CFA in which  $\widehat{\mathbf{Cache}} = (\mathbf{Lab} \times \mathbf{CEnv}) \rightarrow \widehat{\mathbf{Val}}$ . The differences are immaterial for our purposes. See Nielson et al. (1999) for details and a discussion on the use of coinduction in specifying static analyses.

<sup>6</sup>Observe that since closure environments map free variables to contours, the number of closures increases when we increase the contour length  $k$ .

predetermined place in the (abstract) cache. By placing that value in an appropriate context, we construct an instance of the control flow problem: a function  $f$  flows to a call site  $a$  iff the Boolean output is `True`.

Since we have therefore reduced the circuit value problem (Ladner 1975), which is complete for PTIME, to an instance of the OCFA control flow problem, we conclude that the control flow problem is PTIME-hard. Further, as OCFA can be computed in polynomial time, the control flow problem for OCFA is PTIME-complete.

We use some standard syntactic sugar for constructing and deconstructing pairs:

$$\begin{aligned} \langle u, v \rangle &\equiv \lambda z. zuv \\ \text{let } \langle x, y \rangle = p \text{ in } e &\equiv p(\lambda x. \lambda y. e) \end{aligned}$$

Booleans are built out of constants `tt` and `ff`, which are the identity and pair swap function, respectively:

$$\begin{aligned} \text{tt} &\equiv \lambda p. \text{let } \langle x, y \rangle = p \text{ in } \langle x, y \rangle & \text{True} &\equiv \langle \text{tt}, \text{ff} \rangle \\ \text{ff} &\equiv \lambda p. \text{let } \langle x, y \rangle = p \text{ in } \langle y, x \rangle & \text{False} &\equiv \langle \text{ff}, \text{tt} \rangle \end{aligned}$$

The simplest connective is `Not`, which is an inversion on pairs, like `ff`. A *linear* copy connective is defined as:

$$\text{Copy} \equiv \lambda b. \text{let } \langle u, v \rangle = b \text{ in } \langle u(\text{tt}, \text{ff}), v(\text{ff}, \text{tt}) \rangle$$

The coding is easily explained: suppose  $b$  is `True`, then  $u$  is identity and  $v$  twists; so we get the pair `(True, True)`. Suppose  $b$  is `False`, then  $u$  twists and  $v$  is identity; we get `(False, False)`. We write `Copyn` to mean  $n$ -ary fan-out—a straightforward extension of the above.

Now we define truth-table implication:

$$\begin{aligned} \text{Implies} &\equiv \lambda b_1. \lambda b_2. \\ &\quad \text{let } \langle u_1, v_1 \rangle = b_1 \text{ in} \\ &\quad \text{let } \langle u_2, v_2 \rangle = b_2 \text{ in} \\ &\quad \text{let } \langle p_1, p_2 \rangle = u_1 \langle u_2, \text{tt} \rangle \text{ in} \\ &\quad \text{let } \langle q_1, q_2 \rangle = v_1 \langle \text{ff}, v_2 \rangle \text{ in} \\ &\quad \langle p_1, q_1 \circ p_2 \circ q_2 \circ \text{ff} \rangle \end{aligned}$$

Notice that if  $b_1$  is `True`, then  $u_1$  is `tt`, so  $p_1$  is `tt` iff  $b_2$  is `True`. And if  $b_1$  is `True`, then  $v_1$  is `ff`, so  $q_1$  is `ff` iff  $b_2$  is `False`. On the other hand, if  $b_1$  is `False`,  $u_1$  is `ff`, so  $p_1$  is `tt`, and  $v_1$  is `tt`, so  $q_1$  is `ff`. Therefore  $\langle p_1, q_1 \rangle$  is `True` iff  $b_1 \supset b_2$ , and `False` otherwise.<sup>7</sup>

However, simply returning  $\langle p_1, q_1 \rangle$  violates linearity since  $p_2, q_2$  go unused. We know that  $p_2 = \text{tt}$  iff  $q_2 = \text{ff}$  and  $p_2 = \text{ff}$  iff  $q_2 = \text{tt}$ . We do not know which is which, but clearly  $p_2 \circ q_2 = \text{ff} \circ \text{tt} = \text{tt} \circ \text{ff} = \text{ff}$ . Composing  $p_2 \circ q_2$  with `ff`, we are guaranteed to get `tt`. Therefore  $q_1 \circ p_2 \circ q_2 \circ \text{ff} = q_1$ , and we have used all bound variables exactly once. The `And` and `Or` connectives are defined similarly (as in Van Horn and Mairson (2007)).

### 3.1 The Widget

Consider a Boolean circuit coded as a program: it can only evaluate to a (coded) true or false value, but a flow analysis identifies terms by label, so it is possible several different `True` and `False` terms flow out of the program. But our decision problem is defined with respect to a particular term. What we want is to use flow analysis to answer questions like “does this program (possibly) evaluate to a true value?” We use `The Widget` to this effect. It is a term expecting a boolean value. It evaluates as though it were the identity function

<sup>7</sup> Or, if you prefer,  $u_1 \langle u_2, \text{tt} \rangle$  can be read as “if  $u_1$ , then  $u_2$  else `tt`”—the if-then-else description of the implication  $u_1 \supset u_2$ —and  $v_1 \langle \text{ff}, v_2 \rangle$  as its deMorgan dual  $\neg(v_2 \supset v_1)$ . Thus  $\langle p_1, q_1 \rangle$  is the answer we want—and we need only dispense with the “garbage”  $p_2$  and  $q_2$ . DeMorgan duality ensures that one is `tt`, and the other is `ff` (though we do not know which), so they always compose to `ff`.

on Booleans, `Widget`  $b = b$ , but it induces a specific flow we can ask about. If a true value flows out of  $b$ , then `TrueW` flows out of `Widget`  $b$ . If a false value flows out of  $b$ , then `FalseW` flows out of `Widget`  $b$ , where `TrueW` and `FalseW` are distinguished terms, and the only possible terms that can flow out. We usually drop the subscripts and say “does `True` flow out of `Widget`  $b$ ?” without much ado.<sup>8</sup>

$$\text{Widget} \equiv \lambda b. \text{let } \langle u, v \rangle = b \text{ in } \pi_1(u(\text{True}_W, \text{False}_W))$$

Because the circuit value problem is complete for PTIME, we conclude (Van Horn and Mairson 2007):

**THEOREM 1.** *Deciding the control flow problem for OCFA is complete for PTIME.*

## 4. Nonlinearity and Cartesian products: a toy calculation, with insights

A good proof has, at its heart, a small and simple idea that makes it work. For our proof, the key idea is how the approximation of analysis can be *leveraged* to provide computing power *above and beyond* that provided by evaluation. The difference between the two can be illustrated by the following term:

$$\begin{aligned} &(\lambda f. (f \text{ True})(f \text{ False})) \\ &(\lambda x. \text{Implies } x x) \end{aligned}$$

Consider evaluation: Here `Implies`  $x x$  (a tautology) is evaluated twice, once with  $x$  bound to `True`, once with  $x$  bound to `False`. But in both cases, the result is `True`. Since  $x$  is bound to `True` or `False` both occurrences of  $x$  are bound to `True` or to `False`—but it is never the case, for example, that the first occurrence is bound to `True`, while the second is bound to `False`. The values of each occurrence of  $x$  is dependent on the other.

On the other hand, consider what flows out of `Implies`  $x x$  according to ICFA: both `True` and `False`. Why? The approximation incurs analysis of `Implies`  $x x$  for  $x$  bound to `True` and `False`, but it considers *each occurrence of*  $x$  *as ranging over* `True` *and* `False`, *independently*. In other words, for the set of values bound to  $x$ , we consider their *cross product* when  $x$  appears non-linearly. The approximation permits one occurrence of  $x$  be bound to `True` while the other occurrence is bound to `False`; and somewhat alarmingly, `Implies True False` causes `False` to flow out. Unlike in normal evaluation, where within a given scope we know that multiple occurrences of the same variable refer to the same value, in the approximation of analysis, multiple occurrences of the same variable range over *all* values that they are possible bound to *independent of each other*.

Now consider what happens when the program is expanded as follows:

$$\begin{aligned} &(\lambda f. (f \text{ True})(f \text{ False})) \\ &(\lambda x. (\lambda p. p(\lambda u. p(\lambda v. \text{Implies } u v)))(\lambda w. wx)) \end{aligned}$$

Here, rather than pass  $x$  directly to `Implies`, we construct a unary tuple  $\lambda w. wx$ . The tuple is used non-linearly, so  $p$  will range over *closures* of  $\lambda w. wx$  with  $x$  bound to `True` and `False`, again, independently.

A closure can be approximated by an exponential number of values. For example,  $\lambda w. wz_1 z_2 \dots z_n$  has  $n$  free variables, so there are an exponential number of possible environments mapping these variables to program points (contours of length 1). If we could apply a Boolean function to this tuple, we would effectively be evaluating all rows of a truth table; following this intuition leads to NP-hardness of the ICFA control flow problem.

<sup>8</sup> This `Widget` is affine, but this is only for simplicity in presentation. A non-affine widget is given Van Horn and Mairson (2007).

Generalizing from unary to  $n$ -ary tuples in the above example, an exponential number of closures can flow out of the tuple. For a function taking two  $n$ -tuples, we can compute the function on the cross product of the exponential number of closures.

This insight is the key computational ingredient in simulating exponential time, as we describe in the following section.

## 5. The complexity of $k$ CFA

### 5.1 Approximation and EXPTIME

Recall the formal definition of a Turing machine: a 7-tuple

$$\langle Q, \Sigma, \Gamma, \delta, q_0, q_a, q_r \rangle$$

where  $Q$ ,  $\Sigma$ , and  $\Gamma$  are finite sets,  $Q$  is the set of machine states (and  $\{q_0, q_a, q_r\} \subseteq Q$ ),  $\Sigma$  is the input alphabet, and  $\Gamma$  the tape alphabet, where  $\Sigma \subseteq \Gamma$ . The states  $q_0$ ,  $q_a$ , and  $q_r$  are the machine's initial, accept, and reject states, respectively. The complexity class EXPTIME denotes the languages that can be decided by a Turing machine in time exponential in the input length.

Suppose we have a deterministic Turing machine  $M$  that accepts or rejects its input  $x$  in time  $2^{p(n)}$ , where  $p$  is a polynomial and  $n = |x|$ . We want to simulate the computation of  $M$  on  $x$  by  $k$ CFA analysis of a  $\lambda$ -term  $E$  dependent on  $M$ ,  $x$ ,  $p$ , where a particular closure will flow to a specific program point iff  $M$  accepts  $x$ . It turns out that  $k = 1$  suffices to carry out this simulation. The construction, computed in logarithmic space, is similar for all constant  $k > 1$  modulo a certain amount of padding.

### 5.2 Coding machine IDs

The first task is to code machine IDs. Observe that each value stored in the abstract cache  $\widehat{C}$  is a *closure*—a  $\lambda$ -abstraction, together with an environment for its free variables. The number of such abstractions is bounded by the program size, as is the *domain* of the environment—while the number of such *environments* is exponential in the program size. (Just consider a program of size  $n$  with, say,  $n/2$  free variables mapped to only 2 program points denoting bindings.)

Since a closure only has polynomial size, and a Turing machine ID has exponential size, we represent the latter by splitting its information into an exponential number of closures. Each closure represents a tuple  $\langle T, S, H, C, b \rangle$ , which can be read as

*“At time  $T$ , Turing machine  $M$  was in state  $S$ , the tape position was at cell  $H$ , and cell  $C$  held contents  $b$ .”*

$T$ ,  $S$ ,  $H$ , and  $C$  are blocks of bits ( $\mathbf{0} \equiv \text{True}$ ,  $\mathbf{1} \equiv \text{False}$ ) of size polynomial in the input to the Turing machine. As such, each block can represent an exponential number of values. A single machine ID is represented by an exponential number of tuples (varying  $C$  and  $b$ ). Each such tuple can in turn be coded as a  $\lambda$ -term  $\lambda w.wz_1z_2 \dots z_N$ , where  $N = O(p(n))$ .

We still need to be able to generate an exponential number of closures for such an  $N$ -ary tuple. The construction is only a modest, iterative generalization of the construction in our toy calculation above:

$$\begin{aligned} &(\lambda f_1.(f_1 \mathbf{0})(f_1 \mathbf{1})) \\ &(\lambda z_1. \\ & \quad (\lambda f_2.(f_2 \mathbf{0})(f_2 \mathbf{1})) \\ & \quad (\lambda z_2. \\ & \quad \dots \\ & \quad \quad (\lambda f_N.(f_N \mathbf{0})(f_N \mathbf{1})) \\ & \quad \quad (\lambda z_N.((\lambda x.x)(\lambda w.wz_1z_2 \dots z_N))^\ell) \dots)) \end{aligned}$$

In the final subterm  $((\lambda x.x)(\lambda w.wz_1z_2 \dots z_N))^\ell$ , the function  $\lambda x.x$  acts as a very important form of *padding*. Recall that this is  $k$ CFA with  $k = 1$ —the expression  $(\lambda w.wz_1z_2 \dots z_N)$  is evaluated

an exponential number of times—to see why, normalize the term—but in each instance, the contour is always  $\ell$ . (For  $k > 1$ , we would just need more padding to evade the *polyvariance* of the flow analyzer.) As a consequence, each of the (exponential number of) closures gets put in the *same* location of the abstract cache  $\widehat{C}$ , while they are placed in unique, *different* locations of the exact cache  $C$ . In other words, the approximation mechanism of  $k$ CFA treats them as if they are all the same. (That is why they are put in the same cache location.)

### 5.3 Transition function

Now we define a binary transition function  $\delta$ , which does a *piece-meal* transition of the machine ID. The transition function is represented by three rules, identified uniquely by the time stamps  $T$  on the input tuples.

The first *transition rule* is used when the tuples agree on the time stamp  $T$ , and the head and cell address of the first tuple coincide:

$$\delta \langle T, S, H, H, b \rangle \langle T, S', H', C', b' \rangle = \langle T + 1, \delta_Q(S, b), \delta_{LR}(S, H, b), H, \delta_\Sigma(S, b) \rangle$$

This rule *computes* the transition to the next ID. The first tuple has the head address and cell address coinciding, so it has all the information needed to compute the next state, head movement, and what to write in that tape cell. The second tuple just marks that this is an instance of the *computation* rule, simply indicated by having the time stamps in the tuples to be identical. The Boolean functions  $\delta_Q$ ,  $\delta_{LR}$ ,  $\delta_\Sigma$  compute the next state, head position, and what to write on the tape.

The second *communication rule* is used when the tuples have time stamps  $T+1$  and  $T$ : in other words, the first tuple has information about state and head position which needs to be communicated to every tuple with time stamp  $T$  holding tape cell information for an arbitrary such cell, as it gets updated to time stamp  $T+1$ :

$$\delta \langle T + 1, S, H, C, b \rangle \langle T, S', H', C', b' \rangle = \langle T + 1, S, H, C', b' \rangle \quad (H' \neq C')$$

(Note that when  $H' = C'$ , we have already written the salient tuple using the transition rule.) This rule *communicates* state and head position (for the first tuple computed with time stamp  $T+1$ , where the head and cell address coincided) to all the other tuples coding the rest of the Turing machine tape.

Finally, we define a *catch-all rule*, mapping any other pairs of tuples (say, with time stamps  $T$  and  $T+2$ ) to some distinguished null value (say, the initial ID). We need this rule just to make sure that  $\delta$  is a totally defined function.

$$\delta \langle T, S, H, C, b \rangle \langle T', S', H', C', b' \rangle = \text{Null} \quad (T \neq T' \text{ and } T \neq T' + 1)$$

Clearly, these three rules can be coded by a single Boolean circuit, and we have all the required Boolean logic at our disposal.

Because  $\delta$  is a binary function, we need to compute a *cross product* on the coding of IDs to provide its input. The transition function is therefore defined as:

$$\begin{aligned} \Phi &\equiv \lambda p. \\ & \quad \text{let } \langle u_1, u_2, u_3, u_4, u_5 \rangle = \text{Copy}_5 p \text{ in} \\ & \quad \text{let } \langle v_1, v_2, v_3, v_4, v_5 \rangle = \text{Copy}_5 p \text{ in} \\ & \quad (\lambda w.w(\phi_T u_1 v_1)(\phi_S u_2 v_2) \dots (\phi_b u_5 v_5)) \\ & \quad (\lambda w_T.\lambda w_S.\lambda w_H.\lambda w_C.\lambda w_b. \\ & \quad \quad w_T(\lambda z_1.\lambda z_2 \dots \lambda z_T. \\ & \quad \quad \quad w_S(\lambda z_{T+1}.\lambda z_{T+2} \dots \lambda z_{T+S}. \\ & \quad \quad \quad \dots \\ & \quad \quad \quad w_b(\lambda z_{C+1}.\lambda z_{C+2} \dots \lambda z_{C+b=m}. \\ & \quad \quad \quad \lambda w.wz_1z_2 \dots z_m) \dots)) \end{aligned}$$

The Copy functions just copy enough of the input for the separate calculations to be implemented in a linear way. Observe that

this  $\lambda$ -term is entirely linear *except* for the two occurrences of its parameter  $p$ . In that sense, it serves a function analogous to  $\lambda x. \text{Implies } x x$  in the toy calculation. Just as  $x$  ranges there over the closures for **True** and for **False**,  $p$  ranges over all possible IDs flowing to the argument position. Since there are two occurrences of  $p$ , we have two entirely separate iterations in the  $k$ CFA analysis. These separate iterations, like nested “for” loops, create the equivalent of a cross product of IDs in the “inner loop” of the flow analysis.

#### 5.4 Context and widget

The context for the Turing machine simulation needs to set up the initial ID and associated machinery, extract the Boolean value telling whether the machine accepted its input, and feed it into the flow widget that causes different flows depending on whether the value flowing in is **True** or **False**. The following context is used for these purposes:

$$\begin{aligned}
C \equiv & (\lambda f_1.(f_1 \mathbf{0})(f_1 \mathbf{1})) \\
& (\lambda z_1. \\
& \quad (\lambda f_2.(f_2 \mathbf{0})(f_2 \mathbf{1})) \\
& \quad (\lambda z_2. \\
& \quad \dots \\
& \quad (\lambda f_N.(f_N \mathbf{0})(f_N \mathbf{1})) \\
& \quad (\lambda z_N.((\lambda x.x)(\text{Widget}(\text{Extract}[\ ]))^\ell)^{\ell'} \dots)).
\end{aligned}$$

In this code, the  $\lambda x.x$  (with label  $\ell'$  on its application) serve as padding, so that the term within is always applied in the same contour. **Extract** extracts a final ID, with its time stamp, and checks if it codes an accepting state, returning **True** or **False** accordingly. **Widget** is our standard control flow test. The context is instantiated with the coding of the transition function, iterated over an initial machine ID,

$$C[2^n \Phi \lambda w.w \mathbf{0} \dots \mathbf{0} \dots Q_0 \dots H_0 \dots z_1 z_2 \dots z_N \mathbf{0}],$$

where  $\Phi$  is a coding of transition function for  $M$ . The  $\lambda$ -term  $2^n$  is a fixed point operator for  $k$ CFA, which can be assumed to be either **Y**, or an exponential function composer. There just has to be enough iteration of the transition function to produce a fixed point for the flow analysis.

To make the coding easy, we just assume that  $M$  starts by writing  $x$  on the tape, and then begins the generic exponential-time computation. Then we can just have all zeroes on the initial tape configuration.

**LEMMA 2.** *For any Turing machine  $M$  and input  $x$  of length  $n$ , where  $M$  accepts or rejects  $x$  in  $2^{p(n)}$  steps, there exists a logspace-constructible, closed, labeled  $\lambda$ -term  $e$  with distinguished label  $\ell$  such that in the  $k$ CFA analysis of  $e$  ( $k > 0$ ), **True** flows into  $\ell$  iff  $M$  accepts  $x$ .*

**THEOREM 2.** *Deciding the control flow problem for  $k$ CFA with  $k > 0$  is complete for EXPTIME.*

#### 5.5 Exactness and PTIME

At the heart of the EXPTIME-completeness result is the idea that the *approximation* inherent in abstract interpretation is being harnessed for computational power, quite apart from the power of *exact* normalization. To get a good lower bound, this is necessary: it turns out there is a dearth of computation power when  $k$ CFA corresponds with normalization, i.e. when the analysis is exact.

As noted earlier, approximation arises from the truncation of contours during analysis. Consequently, if truncation never occurs, the instrumented interpreter and the abstract interpreter produce identical results for the given program. But what can we say about

the complexity of these programs? In other words, what kind of computations can  $k$ CFA analyze exactly when  $k$  is a constant, independent of the program analyzed?

An answer to this question provides another point in the characterization of the expressiveness of an analysis. For 0CFA, the answer is PTIME since the evaluation of linear terms is captured. For  $k$ CFA, the answer remains the same: for any fixed  $k$ ,  $k$ CFA can only analyze polynomial time programs exactly. It is only through the use of approximation that a exponential time computation can be simulated, but this computation has little to do with the actual running of the program. A program that runs for exponential time cannot be analyzed exactly by any fixed  $k$ CFA. Contrast this with ML-typability, for example, where the evaluation of programs that run for exponential time can be simulated via type inference.

Note that if the contour is never truncated, every program point is now approximated by at most one closure (rather than an exponential number of closures). The size of the cache is then bounded by a polynomial in  $n$ ; since the cache is computed monotonically, the analysis and the natural related decision problem is constrained by the size and use of the cache.

**PROPOSITION 1.** *Deciding the control flow problem for exact  $k$ CFA is complete for PTIME.*

This proposition provides a characterization of the computational complexity (or expressivity) of the language evaluated by the instrumented evaluator  $\mathcal{E}$  of section 2.1 as a function of the contour length.

It also provides an analytic understanding of the empirical observation researchers have made: computing a more precise analysis is often cheaper than performing a less precise one, which “yields coarser approximations, and thus induces more merging. More merging leads to more propagation, which in turn leads to more reevaluation” (Wright and Jagannathan 1998). Might and Shivers (2006) make a similar observation: “imprecision reinforces itself during a flow analysis through an ever-worsening feedback loop.” This ever-worsening feedback loop, in which we can make **False** (spuriously) flow out of **Implies**  $x x$ , is the critical ingredient in our EXPTIME lower bound.

Finally, the asymptotic differential between the complexity of exact and abstract interpretation shows that abstract interpretation is strictly more expressive, for any fixed  $k$ .

#### 5.6 Discussion

We observe an “exponential jump” between contour length and complexity of the control flow decision problem for every polynomial-length contour, including contours of constant length. Once  $k = n$  (contour length equals program size), an exponential-time hardness result can be proved which is essentially a linear circuit with an exponential iterator—very much like (Mairson 1990). When the contours are exponential in program length, the decision problem is doubly exponential, and so on.

The reason for this exponential jump is the cardinality of environments in closures. This, in fact, is the bottleneck for control flow analysis—it is the reason that 0CFA (without closures) is tractable, while 1CFA is not. If  $f(n)$  is the contour length and  $n$  is the program length, then

$$|\mathbf{CEnv}| = |\mathbf{Var} \rightarrow \Delta^{\leq f(n)}| = (n^{f(n)})^n = 2^{f(n)n \lg n}$$

This cardinality of environments effectively determines the size of the universe of values for the abstract interpretation realized by CFA.

When  $k$  is a constant, one might ask why the inherent complexity is exponential time, and not more—especially since one can iterate (in an untyped world) with the **Y** combinator. Exponential time is the “limit” because with a polynomial-length tuple (as con-

strained by a logspace reduction), you can only code an exponential number of closures.

Finally, we need to emphasize the importance of linearity in static analysis. Static analysis makes approximations to be tractable, but with linear terms, there is not approximation. We carefully admitted a certain, limited nonlinearity in order to increase the lower bound.

## 6. Related work

Our earlier work on the complexity of compile-time type inference is a precursor of the research insights described here, and naturally so, since type inference is a kind of static analysis. The decidability of type inference depends on the making of approximations, necessarily rejecting programs without type errors; in simply-typed  $\lambda$ -calculus, for instance, all occurrences of a variable must have the same type. (The same is, in effect, also true for ML, modulo the finite development implicit in `let`-bindings.) The type constraints on these multiple occurrences are solved by first-order unification.

As a consequence, we can understand the inherent complexity of type inference by analyzing the expressive power of *linear* terms, where no such constraints exist, since linear terms are always simply-typable. In these cases, type inference is synonymous with normalization.<sup>9</sup> This observation motivates the analysis described in Mairson (1990, 2004).

The intuition behind the correspondence between evaluation and flow analysis for linear terms can be seen as an instance of *abstract counting* in the extreme (Might and Shivers 2006). Abstract counting is a technique for reasoning about the behavior of a program that *must* occur when a program is run, based solely on abstract information that describes what *may* occur. When an abstract value is a singleton set, the abstract object is effectively rendered concrete (Jagannathan et al. 1998). In other words, when only one thing may happen, it must. Linearity maintains singularity, and analysis is therefore completely concrete.

Our coding of Turing machines is descended from earlier work on Datalog (Prolog with variables, but without constants or function symbols), a programming language that was of considerable interest to researchers in database theory during the 1980s; see Hillebrand et al. (1995); Gaifman et al. (1993).

In *k*CFA and abstract interpretation more generally, an expression can evaluate to a set of values from a finite universe, clearly motivating the idiom of programming with sets. Relational database queries take as input a finite set of tuples, and compute new tuples from them; since the universe of tuples is finite and the computation is monotone, a fixed-point is reached in a finite number of iterations. The machine simulation here follows that framework very closely. Even the idea of splitting a machine configuration among many tuples has its ancestor in (Hillebrand et al. 1995), where a ternary `cons(A, L, R)` is used to simulate a cons-cell at memory address *A*, with pointers *L, R*. It needs emphasis that the computing with sets described in this paper has little to do with normalization, and everything to do with the approximation inherent in the abstract interpretation.

This coding of Boolean logic in linear  $\lambda$ -calculus, which was previously given in Van Horn and Mairson (2007) and is briefly described again here for completeness, improves upon Mairson (2004) in that it allows uniform typing, and does not create garbage. The encoding in Mairson (2004) in turn is an improvement of the Church encodings in that they are linear and non-affine.

<sup>9</sup> An aberrant case of this phenomenon is analyzed in Neergaard and Mairson (2004), which analyzed a type system where normalization and type inference are synonymous in *every* case. The tractability of type inference thus implied a certain inexpressiveness of the language.

Although *k*CFA and ML type inference are two static analyses complete for EXPTIME (Mairson 1990), the proofs of these respective theorems is fundamentally different. The ML proof relies on type inference simulating exact normalization (analogous to the PTIME-completeness proof for 0CFA), hence subverting the approximation of the analysis. In contrast, the *k*CFA proof harnesses the approximation that results from nonlinearity.

Recent work by Might (2007) and Might and Shivers (2006) has examined various techniques for reducing the imprecision of flow analysis via *abstract garbage collection* and *abstract counting*. Might and Shivers observe that by eliminating spurious flows, not only is the precision improved, but this often leads to improved running times of the analyzer. Our theorems reinforce these observations and shed light on what might otherwise seem like a paradoxical situation: “in many cases, higher speed is a direct consequence of higher precision.” When the analysis is at its most precise, it is always computable quickly. On the other hand, wielding the full power of spurious flows results in EXPTIME-completeness. In essence, *k*CFA is hard *because of* the spurious flows that it must compute. In this light, techniques such as abstract garbage collection undermine our lower bound proofs, making it unclear what complexity bounds exist for these enhanced analyses.

## 7. Conclusions and perspective

Empirically observed increases in costs can be understood analytically as *inherent in the approximation problem being solved*.

We have given an exact characterization of the *k*CFA approximation problem. The EXPTIME lower bound validates empirical observations and proves there is no tractable algorithm for *k*CFA.

The proof relies on previous insights about linearity, static analysis, and normalization (namely, when a term is linear, static analysis and normalization are synonymous); coupled with new insights about using non-linearity to realize the full computational power of approximate, or abstract, interpretation.

Shivers wrote in his best of PLDI retrospective (2004),

*Despite all this work on formalising CFA and speeding it up, I have been disappointed in the dearth of work extending its power.*

This work has shown that work spent on speeding up *k*CFA is an exercise in futility; there is no getting around the exponential bottleneck of *k*CFA. The one-word description of the bottleneck is *closures*, which do not exist in 0CFA, because free variables in a closure would necessarily map to  $\epsilon$ , and hence the environments are useless.

As for extending its power, from a complexity perspective, we can see that 0CFA is strictly less expressive than *k*CFA. In turn, *k*CFA is strictly less expressive than, for example, Mossin’s flow analysis (1997). Mossin’s analysis is a stronger analysis in the sense that it is exact for a larger class of programs than 0CFA or *k*CFA—it exact not only for linear terms, but for all simply-typed terms. In other words, the flow analysis of simply-typed programs is synonymous with running the program, and hence non-elementary. This kind of expressivity is also found in Burn-Hankin-Abramsky style strictness analysis (1985). But there is a considerable gap between *k*CFA and these more expressive analyses. What is in between and how can we build a real *hierarchy* of static analyses that occupy positions within this gap?

Lévy’s notion of labeled reduction (1978) provides a richer notion of “instrumented evaluation” coupled with a richer theory of exact flow analysis, namely the geometry of interaction. With the proper notion of abstraction and simulated reduction, we should be able to design more powerful flow analyses, filling out the hierarchy from 0CFA up to the expressivity of Mossin’s analysis in the limit.

**Acknowledgments** Thanks to Matt Might, Olin Shivers, and Mitch Wand for listening to preliminary presentations of this proof.

## References

- G. L. Burn, C. L. Hankin, and S. Abramsky. The theory of strictness analysis for higher order functions. In H. Ganzinger and N. Jones, editors, *Programs as data objects*, pages 42–62, New York, NY, USA, 1985. Springer-Verlag. ISBN 0-387-16446-4.
- Haim Gaifman, Harry Mairson, Yehoshua Sagiv, and Moshe Y. Vardi. Undecidable optimization problems for database logic programs. *J. ACM*, 40(3):683–713, 1993. ISSN 0004-5411. doi: <http://doi.acm.org/10.1145/174130.174142>.
- Fritz Henglein. Simple closure analysis. DIKU Semantics Report D-193, March 1992.
- Gerd G. Hillebrand, Paris C. Kanellakis, Harry G. Mairson, and Moshe Y. Vardi. Undecidable boundedness problems for datalog programs. *J. Logic Program.*, 25(2):163–190, 1995.
- Suresh Jagannathan, Peter Thiemann, Stephen Weeks, and Andrew Wright. Single and loving it: must-alias analysis for higher-order languages. In *POPL '98: Proceedings of the 25th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, pages 329–341, New York, NY, USA, 1998. ACM. ISBN 0-89791-979-3. doi: <http://doi.acm.org/10.1145/268946.268973>.
- Neil D. Jones. Flow analysis of lambda expressions (preliminary version). In *Proceedings of the 8th Colloquium on Automata, Languages and Programming*, pages 114–128, London, UK, 1981. Springer-Verlag. ISBN 3-540-10843-2.
- Richard E. Ladner. The circuit value problem is log space complete for  $P$ . *SIGACT News*, 7(1):18–20, 1975. ISSN 0163-5700. doi: <http://doi.acm.org/10.1145/990518.990519>.
- Jean-Jacques Lévy. *Réductions correctes et optimales dans le lambda-calcul*. PhD thesis, Paris 7, January 1978. Thèse d'Etat.
- Harry G. Mairson. Linear lambda calculus and PTIME-completeness. *J. Funct. Program.*, 14(6):623–633, 2004. ISSN 0956-7968. doi: <http://dx.doi.org/10.1017/S0956796804005131>.
- Harry G. Mairson. Deciding ML typability is complete for deterministic exponential time. In *POPL '90: Proceedings of the 17th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, pages 382–401, New York, NY, USA, 1990. ACM. ISBN 0-89791-343-4. doi: <http://doi.acm.org/10.1145/96709.96748>.
- Jan Midtgaard. Control-flow analysis of functional programs. Technical Report BRICS RS-07-18, DAIMI, Department of Computer Science, University of Aarhus, Aarhus, Denmark, December 2007.
- Matthew Might. *Environment analysis of higher-order languages*. PhD thesis, Georgia Institute of Technology, Atlanta, GA, USA, 2007. Adviser: Olin G. Shivers.
- Matthew Might and Olin Shivers. Improving flow analyses via GCFA: abstract garbage collection and counting. In *ICFP '06: Proceedings of the eleventh ACM SIGPLAN international conference on Functional programming*, pages 13–25, New York, NY, USA, 2006. ACM. ISBN 1-59593-309-3. doi: <http://doi.acm.org/10.1145/1159803.1159807>.
- Christian Mossin. Exact flow analysis. In *SAS '97: Proceedings of the 4th International Symposium on Static Analysis*, pages 250–264, London, UK, 1997. Springer-Verlag. ISBN 3-540-63468-1.
- Peter Møller Neergaard and Harry G. Mairson. Types, potency, and idempotency: why nonlinearity and amnesia make a type system work. In *ICFP '04: Proceedings of the ninth ACM SIGPLAN International Conference on Functional Programming*, pages 138–149, New York, NY, USA, 2004. ACM Press. ISBN 1-58113-905-5. doi: <http://doi.acm.org/10.1145/1016850.1016871>.
- Flemming Nielson and Hanne Riis Nielson. Infinitary control flow analysis: a collecting semantics for closure analysis. In *POPL '97: Proceedings of the 24th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, pages 332–345, New York, NY, USA, 1997. ACM. ISBN 0-89791-853-3. doi: <http://doi.acm.org/10.1145/263699.263745>.
- Flemming Nielson, Hanne R. Nielson, and Chris Hankin. *Principles of Program Analysis*. Springer-Verlag New York, Inc., Secaucus, NJ, USA, 1999. ISBN 3540654100.
- Peter Sestoft. Replacing function parameters by global variables. Master's thesis, DIKU, University of Copenhagen, Denmark, October 1988. Master's thesis no. 254.
- Olin Shivers. Control flow analysis in Scheme. In *PLDI '88: Proceedings of the ACM SIGPLAN 1988 Conference on Programming Language Design and Implementation*, pages 164–174, New York, NY, USA, 1988. ACM. ISBN 0-89791-269-1. doi: <http://doi.acm.org/10.1145/53990.54007>.
- Olin Shivers. *Control-flow Analysis of Higher-order Languages*. PhD thesis, Carnegie Mellon University, Pittsburgh, PA, USA, 1991.
- Olin Shivers. Higher-order control-flow analysis in retrospect: lessons learned, lessons abandoned. *SIGPLAN Not.*, 39(4):257–269, 2004. ISSN 0362-1340. doi: <http://doi.acm.org/10.1145/989393.989421>.
- David Van Horn and Harry G. Mairson. Relating complexity and precision in control flow analysis. In *ICFP '07: Proceedings of the 2007 ACM SIGPLAN International Conference on Functional Programming*, pages 85–96, New York, NY, USA, 2007. ACM Press. ISBN 978-1-59593-815-2. doi: <http://doi.acm.org/10.1145/1291151.1291166>.
- David Van Horn and Harry G. Mairson. Linearity, flow analysis, and PTIME. In María Alpuente and Germán Vidal, editors, *The 15th International Static Analysis Symposium, SAS 2008*, volume 5079 of *Lecture Notes in Computer Science*, pages 255–269. Springer, July 2008.
- Andrew K. Wright and Suresh Jagannathan. Polymorphic splitting: an effective polyvariant flow analysis. *ACM Trans. Program. Lang. Syst.*, 20(1):166–207, 1998. ISSN 0164-0925. doi: <http://doi.acm.org/10.1145/271510.271523>.