A Compositional Trace Semantics for Orc

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- Sites: autonomous computing units that do arithmetic, printing etc
- Operators to combine the executions of sites e.g. parallel composition
- Recursive declarations to express nonterminating processes
 - can encode many popular concurrent programming patterns

The simplest Orc process is a site call:

Factorize (N)

Reddit (Oct 20)

Symmetric Composition $(f \mid g)$: evaluate f and g in parallel, no interaction between them

Factorize (N) | Reddit (Oct 20)

Sequencing (f > x > g): evaluate f, when it publishes spawn a new instance of g in parallel

(Factorize(N) | Reddit(Oct'20)) >x> Print(x)

■ Asymmetric Composition (f where x:€ g): evaluate f and g in parallel, when g publishes it sends the value to f and terminates

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Print(x) where x:€
(Factorize(N)|Reddit(Oct'20))
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Recursive Declarations $(E_{i}(x) = f_{i})$: We can express processes that don't terminate

We define:

$$DOS(x) = Ping(x) \mid DOS(x)$$

And then call: DOS (ip)

Syntax and Operational Semantics

Syntax

$$P := D_1, ..., D_k$$
 in e

e ::= 0 | M(p) | let(p)
|
$$E_i(p)$$
 | $(e_1|e_2)$
| $e_1 > x > e_2$
| e_1 where $x : \in e_2$

Actual

$$D_i$$
 ::= $E_i(x) = e$

Labeled transitions

$$\Delta, \Gamma \vdash f \xrightarrow{a} f'$$

(SITEC)
$$\overline{\Delta, \Gamma \vdash M(v)} \xrightarrow{M_k(v)} ?k \text{ fresh}$$
(SITEC-VAR)
$$\overline{\Delta, \Gamma \vdash M(x)} \xrightarrow{[v/x]} M(v) \qquad \Gamma(x) = v$$
(SITERET)
$$\overline{\Delta, \Gamma \vdash ?k} \xrightarrow{k?v} let(v)$$

(LET)
$$\frac{\Delta, \Gamma \vdash let(v) \xrightarrow{!v} \mathbf{0} }{ \Delta, \Gamma \vdash let(x) \xrightarrow{[v/x]} let(v) } \Gamma(x) = v$$

(DEF)
$$\frac{\Delta, \Gamma \vdash E_{i}(v) \xrightarrow{\tau} [v/x] f_{i}}{\Delta, \Gamma \vdash E_{i}(x) \xrightarrow{E_{i}(v)} E_{i}(v)} (E_{i}(x) \triangleq f_{i}) \in \Delta$$
(DEF-VAR)
$$\frac{\Delta, \Gamma \vdash E_{i}(x) \xrightarrow{[v/x]} E_{i}(v)}{\Delta, \Gamma \vdash E_{i}(x) \xrightarrow{E_{i}(v)} E_{i}(v)} (E_{i}(x) \triangleq f_{i}) \in \Delta$$

(SYM-L)
$$\frac{\Delta, \Gamma \vdash f \xrightarrow{a} f'}{\Delta, \Gamma \vdash f \mid g \xrightarrow{a} f' \mid g}$$
(SYM-R)
$$\frac{\Delta, \Gamma \vdash g \xrightarrow{a} g'}{\Delta, \Gamma \vdash f \mid g \xrightarrow{a} f \mid g'}$$

(SEQ)
$$\frac{\Delta, \Gamma \vdash f \xrightarrow{a} f'}{\Delta, \Gamma \vdash f > x > g \xrightarrow{a} f' > x > g} a \neq !v$$
(SEQ-P)
$$\frac{\Delta, \Gamma \vdash f \xrightarrow{v} f'}{\Delta, \Gamma \vdash f > x > g \xrightarrow{\tau} (f' > x > g) \mid [v/x]g}$$

(ASYM-L)
$$\frac{\Delta, \Gamma \vdash f \xrightarrow{a} f'}{\Delta, \Gamma \vdash f \text{ where } x :\in g \xrightarrow{a} f' \text{ where } x :\in g} a \neq [v/x]$$
(ASYM-R)
$$\frac{\Delta, \Gamma \vdash g \xrightarrow{a} g'}{\Delta, \Gamma \vdash f \text{ where } x :\in g \xrightarrow{a} f \text{ where } x :\in g'} a \neq !v$$
(ASYM-P)
$$\frac{\Delta, \Gamma \vdash g \xrightarrow{!v} g'}{\Delta, \Gamma \vdash f \text{ where } x :\in g \xrightarrow{\tau} [v/x]f}$$

More examples: Fork-join Parallelism

 Launch two threads and wait for both to complete before you proceed

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(let(x,y) where x:\in M(v<sub>1</sub>)) where y:\in N(v<sub>2</sub>)
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More examples: Parallel-or

 Call M and N in parallel, if one replies "true" don't wait for the other to reply

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((let(z) where z:\varepsilon ift(x)|ift(y)|or(x,y) where x:\varepsilon M(v<sub>1</sub>)) where y:\varepsilon N(v<sub>2</sub>))
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- Trace Semantics
- Based on complete partial orders

$$\llbracket f \rrbracket : [Fenv \to [Env \to P]]$$

- ([2/x] !2) is a possible trace of let(x)
- (!3 !5) and (!5 !3) are possible traces of (let(3) |
 let(5))
- (let(x) | let(7)) where x:€ let(2)
 In a trace of (let(x) | let(7)), how do we know if
 let(7) publishes before let(2) sends a value?



Receive events show what part of the trace is independent of some variable!

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e.g. ([2/x] !2 !7) and (!7 [2/x] !2) are possible traces of (let(x) | let(7))
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!7 is independent of [2/x] because it can happen before [2/x]

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Then, (\tau !2 !7) and (!7 \tau !2) are possible traces of (let(x) | let(7)) where x : \varepsilon let(2) but not (!7 !2 \tau)
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We do not need this information in (let(2) > x) (let(x) | let(7)) because when the right hand side is launched, x always has a value

Then, do we put the receive event in the traces of let(x) or not?

Two kinds of bindings for variables in the environment

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[M(v)] = \lambda \varphi. \lambda \rho. \{ M_k(v) \ k?w \ !w \ | \ k \ \text{fresh} \ , w \in Val \}_{p}
[M(x)] = \lambda \varphi. \lambda \rho. \mathbf{case} \ \rho(x) \ \mathbf{of} \ \text{Absent.} \{ \varepsilon \}
\forall v. \{ M_k(v) \ k?w \ !w \ | \ k \ \text{fresh} \ , w \in Val \}_{p}
\forall v. \{ [v/x] \ M_k(v) \ k?w \ !w \ | \ k \ \text{fresh} \ , w \in Val \}_{p}
[?k] = \lambda \varphi. \lambda \rho. \{ \ k?w \ !w \ | \ w \in Val \}_{p}
```

$$\llbracket h \mid g \rrbracket = \lambda \varphi. \lambda \rho. \llbracket h \rrbracket \varphi \rho \parallel \llbracket g \rrbracket \varphi \rho$$

Merge:

$$t_1 \parallel t_2 \triangleq \begin{cases} \{t_1\} & t_2 = \varepsilon \\ \{t_2\} & t_1 = \varepsilon \\ a(t_1' \parallel t_2) \cup b(t_1 \parallel t_2') & t_1 = at_1' \text{ and } t_2 = bt_2' \end{cases}$$

Merge trace-sets:

$$T_1 \| T_2 \triangleq \bigcup_{t_1 \in T_1, t_2 \in T_2} t_1 \| t_2$$

$$[\![h>x>g]\!] = \lambda \varphi.\lambda \rho. \bigcup_{s\in [\![h]\!] \varphi \rho} s \gg \lambda v. [\![g]\!] \varphi \rho [x=\flat v]$$

Sequencing combinator:

$$s \gg F = \begin{cases} \{s\} & \text{no publ. in } s \\ s_1 \tau ((s_2 \gg F) \parallel F(v)) & s \equiv s_1! v s_2, \text{ no publ. in } s_1 \end{cases}$$

$$\llbracket h \text{ where } x :\in g \rrbracket = \lambda \varphi. \lambda \rho. \ (\bigcup_{v \in Val} \ \llbracket h \rrbracket \varphi \rho [x = \natural v]) <_x \ \llbracket g \rrbracket \varphi \rho$$

Asymmetric combinator:

$$t_1 <_x t_2 = \begin{cases} t_1 \parallel t_2 & \text{no recv. for } x \text{ in } t_1 \text{ , no publ. in } t_2 \\ t_1 \parallel t_{21}\tau & \text{no recv. for } x \text{ in } t_1 \text{ , } t_2 \equiv t_{21}! v \, t_{22} \text{ , no publ. in } t_{21} \\ (t_{11} \parallel t_{21}\tau)(t_{12} \setminus [v/x]) & t_1 \equiv t_{11}[v/x]t_{12} \text{ , no recv. for } x \text{ in } t_{11} \text{ ,} \\ t_2 \equiv t_{21}! v \, t_{22} \text{ , no publ. in } t_{21} \\ \{\varepsilon\} & \text{otherwise} \end{cases}$$

Asymmetric combinator for trace-sets:

$$T_1 <_x T_2 = \bigcup_{t_1 \in T_1, t_2 \in T_2} t_1 <_x t_2$$

Semantic Properties

- Continuity of the meaning functions
- Prefix-closure of the trace sets
- Adequacy:

$$t \in [\![f]\!] [\![\Delta]\!] \rho \quad \textit{iff} \quad \exists f'. \ \Delta, \Gamma \vdash \ \sigma f \stackrel{t}{\rightarrow}^* f'$$

What to remember about Orc

- Abstracts computation away, focuses on communication
- Small but expressive
- Interesting theoretical properties

Related Work

- Kitchin, Cook and Misra. "A language for task orchestration and its semantic properties", CONCUR (2006)
- van der Aalst et al. "Workflow Patterns", Distributed and Parallel Databases 14(1): 5-51 (2003)
- Cook, Patwardhan and Misra. "Workflow patterns in Orc", COORD (2006)
- Misra and Cook. "Computation Orchestration: a basis for wide-area computing", Software and Systems Modeling, 6(1): 83-110 (2007)

A peek at what follows

- Proved useful congruences between Orc processes using strong bisimulation
- Created semantics insensitive to internal events, we can equate more processes

More info:

http://www.ccs.neu.edu/home/dimvar

