

1 Representation of instructions - Spring, '07

For this question, write any addresses that are part of your answer in hexadecimal.

1.1 a.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
001000	01000	10000	11111	11111	111111

.

1.2 b.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
000000	01001	01010	00100	00000	100010

.

1.3 c.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
000010	00000	00000	00000	00001	000000

.

2 Representation of instructions - Fall, '07

For this question, write any addresses that are part of your answer in hexadecimal.

2.1 a.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
000000	00000	01010	00100	00000	101010

.

2.2 b.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
001000	00100	10000	11111	11111	111110

.

2.3 c.

Write one line of assembly language that will assemble into the following instruction:

6 bits	5 bits	5 bits	5 bits	5 bits	6 bits
000010	00000	00000	00000	00000	100000

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