

## Written Homework 01

**Assigned:** Wed 23 Sep 2009

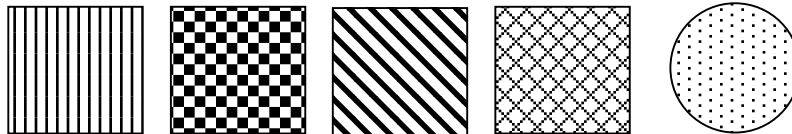
**Due:** Wed 30 Sep 2009

### Instructions:

- The assignment is due at the *beginning* of class on the due date specified. Late assignments will be penalized 50%, as stated in the course information sheet. Late assignments *will not be accepted* after the solutions have been distributed.
- We expect that you will study with friends and often work out problem solutions together; however, you must write up your own solutions, in your own words. Cheating will not be tolerated.
- We expect your homework to be neat, organized, and legible. If your handwriting is unreadable, please type your solutions. Use 8.5in by 11in loose-leaf or printer paper, and please do not hand in sheets that have been ripped from spiral bound notebooks.
- Problem 1 requires graph paper. Printable graph paper may be found at the following URL:

<http://www.printfreegraphpaper.com/gp/c-i-14.pdf>

**Problem 1** [24 pts, (16,8)]: Patterns.

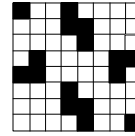
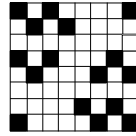
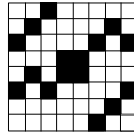
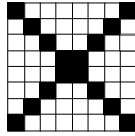


Patterns, like the ones above, are available in most drawing programs for filling regions. A pattern is often defined by an  $8 \times 8$  array of bits. In each of the following two examples, the  $8 \times 8$  array of bits on the left corresponds to the pattern on the right.



The 0s represent white, and the 1s represent black. Each row is an 8-bit binary number. As we know, a 4-bit binary number can be expressed as a single hex-digit, so an 8-bit binary number can be expressed with two hex-digits. Designers specify a pattern by giving eight 2-hex-digit numbers, one 2-hex-digit number per row. The two patterns given above are encoded as “11, 11, 11, 11, 11, 11, 11, 11” and “33, 33, CC, CC, 33, 33, CC, CC.”

- i. For each of the following patterns, give the eight 2-hex-digit encoding.



- ii. Use graph paper to show the pattern described by each of the following sequences of eight 2-hex-digit numbers. (See the instructions above for a link to printable graph paper.)

08, 19, 2A, 3B, 4C, 5D, 6E, 7F

87, 96, A5, B4, C3, D2, E1, F0

**Problem 2** [32 pts, (8,8,8,8)]: Negative numbers and two's complement.

In each of the parts below, show your work.

- i. Give the 8-bit two's complement representations of the following integers: 27, 83,  $-75$ ,  $-69$ .
- ii. Give the integer (in standard base-10 notation) which is represented by each of the following 8-bit two's complement numbers: 10110011, 01100011, 01101111, 10001111.
- iii. Compute the following sums and differences using 8-bit two's complement representations, as shown in class and described in the text:  $-69 + 27$ ,  $83 - 75$ , and  $-75 - 69$ . In each case, indicate whether the calculation results in an overflow. If it does not result in an overflow, then verify that your answer is correct by converting the result back to standard base-10 notation.

*Note:* Use the two's complement representations from part i above.

- iv. What range of numbers can be represented in (a) 6-bit two's complement and (b) 18-bit two's complement? What are the *minimum* number of bits necessary to represent (c) 2010 and (d)  $-97$  in two's complement?

**Problem 3** [20, (4 pts each)]: Multiplication.

Perform the following multiplications in binary. For each problem part, you must (1) convert each decimal number to binary, (2) perform the multiplication in binary, and (3) convert the binary result back to decimal. You must show your work.

*Note:* For consistency, place the binary representation of the left multiplicand in the top row of your multiplication and place the binary representation of the right multiplicand on the bottom row of your multiplication. Thus, " $4 \times 7$ " would be

$$\begin{array}{r} 100 \\ \times 111 \\ \hline \end{array}$$

while " $7 \times 4$ " would be

$$\begin{array}{r} 111 \\ \times 100 \\ \hline \end{array}$$

by this convention.

- i.  $51 \times 10$
- ii.  $51 \times 7$
- iii.  $42 \times 16$
- iv.  $42 \times 15$

Just as in addition, multiplication of two binary numbers may result in overflow if the size of the representation is not large enough.

- v. What is the maximum number of bits needed to represent the multiplication of a 9-bit number with a 7-bit number? Justify your answer.

**Problem 4** [24, (4,4,4,6,6)]: Logic and Logical Equivalence.

A *majority gate* is a  $(2n + 1)$ -input, 1-output device that takes an *odd* number of bits as input and returns the *majority vote* as output. For example, a 3-input, 1-output majority gate will return 1 if two or more inputs bits are 1; otherwise, it will return 0.

- i. Construct the truth table for a 3-input, 1-output majority gate. Label your input bits  $A$ ,  $B$ , and  $C$ , and label your output bit  $M$ .
- ii. Using the DNF construction described in class, construct the *Boolean formula* corresponding to a 3-input, 1-output majority gate.

If you look carefully at the truth table and circuit for the full adder, as described in class and in the text, you will note that the carry-out bit  $C_o$  is exactly the majority vote of the three input bits  $A$ ,  $B$ , and  $C_i$ . However, the circuit (and corresponding Boolean formula) that implements the majority function for the carry-out bit  $C_o$  is quite different than your DNF construction above.

- iii. Give the Boolean formula that corresponds to the circuit (as shown in class and in the text) for the full adder carry-out bit. Use  $A$ ,  $B$ , and  $C$  as variables in your Boolean formula (i.e., replace  $C_i$  from the circuit diagram with simply  $C$ ). Note that  $A \oplus B \equiv (A \wedge \neg B) \vee (\neg A \wedge B)$ , as can easily be seen from the DNF construction applied to the XOR truth table.
- iv. Using the Laws of Logic, show that your Boolean formulae from parts ii and iii above are logically equivalent. In other words, start with your Boolean formula from part ii (obtained via the DNF construction) and apply the Laws of Logic until you derive your Boolean formula from part iii.
- v. The simplest Boolean formula (and corresponding circuit) for the majority function is

$$M = (A \wedge B) \vee (A \wedge C) \vee (B \wedge C).$$

Again, using the Laws of Logic, show that your Boolean formula from part ii (obtained via the DNF construction) is logically equivalent to the formula given above.

*Hint:* The formula you obtain via the DNF construction in part ii will contain four terms (variables or their negations that are ANDed together), and one of these terms is  $(A \wedge B \wedge C)$ . Replicate this term three times, obtaining

$$(term_1) \vee (term_2) \vee (term_3) \vee (A \wedge B \wedge C) \vee (A \wedge B \wedge C) \vee (A \wedge B \wedge C).$$

(Why does this not change the function computed by the formula?) Now group each of your other terms together with a single copy of  $(A \wedge B \wedge C)$

$$\left[ (term_1) \vee (A \wedge B \wedge C) \right] \vee \left[ (term_2) \vee (A \wedge B \wedge C) \right] \vee \left[ (term_3) \vee (A \wedge B \wedge C) \right]$$

and apply the Laws of Logic to each of your groupings.